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MAY 06 2005**PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION

App./Confirm Nos.: 09/683,985 2597

Applicant: Steven Voldman

Filing Date: March 8, 2002

Title: **A METHOD AND STRUCTURE FOR LOW**
CAPACITANCE ESD ROBUST DIODES

Atty. Docket: BUR9-2002-0014-US1

Today's Date: May 6, 2005

Examiner: Ori Nadav

Group Art Unit: 2811

Fax: 703-872-9306

Amendments Under 37 C.F.R 1.114**Amendment C**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated February 9, 2005, Applicant respectfully requests reconsideration of the outstanding rejections and reexamination of the present application in light of the following remarks and amendments.

CERTIFICATE OF MAILING	
I hereby certify that, on the date shown below, this correspondence is being:	
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<input type="checkbox"/> deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450	<input checked="" type="checkbox"/> transmitted by facsimile to the Patent and Trademark Office
Date: <u>05/06/2005</u>	<u>ANTHONY J. CANALE</u> Name
	<u>Anthony J. Canale</u> Signature

BUR9-2002-0014-US1